

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Warnakulasooriya et al.	
Application No.:	Group Art Unit: 2835
Filed: Herewith	Examiner:
Title: Arbitration Mechanism for a Dual Serial Bus Architecture	
Attorney Docket No.: 130-022 EMC-03-060	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

This application is a continuation-in-part application claiming priority to application serial no. 10/608,635 filed June 27, 2003, currently pending.

Respectfully Submitted,

2/4/2004
Date

Lindsay G. McGuinness
Lindsay G. McGuinness, Reg. No. 38,549
Attorney/Agent for Applicant(s)
Steubing McGuinness & Manaras LLP
125 Nagog Park Drive
Acton, MA 01720
(978) 264-6664

Docket No. 130-022